

“ LC DIP 20 ”

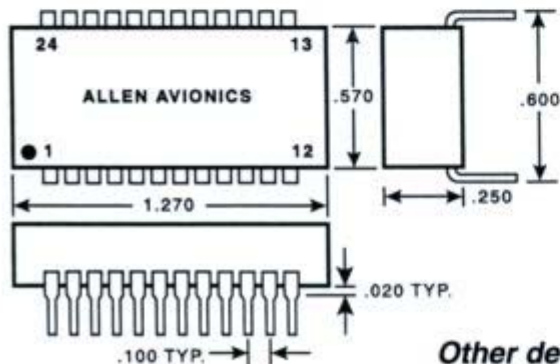
Dual-In-Line 24-Pin Tapped Lumped Constant Delay Lines



- **High Bandwidths**
- **Low Rise Time**
- **Small Package**
- **Excellent Pulse Fidelity**
- **Low Attenuation**
- **Compatibility With TTL & DTL**

- **Time delay to rise time ratio: 10-to-1.**
- **Temperature Coefficient: less than 100PPM/°C from 0°C to 80°C.**
- **Total delay tolerance 5% or 1 nanosecond, whichever is greater.**
- **20 Taps of equal increments.**
- **TTL & DTL compatible.**
- **Low attenuation.**
- **Exceeds applicable portions of MIL-D-23859**
- **Voltage rating 50 WVDC**

Part No.	Time Delay (Nanoseconds)	Rise Time (Nanoseconds)	3dB Bandwidth Minimum (MHz)	Attenuation Maximum (%)	Wiring Diagram No.
IMPEDANCE 50 OHMS					
LCB020Z050	20	3.0	120.0	3	2
LCB030Z050	30	3.5	102.8	3	2
LCB040Z050	40	4	90.0	4	1
LCB050Z050	50	5	72.0	4	1
LCB060Z050	60	6	60.0	5	1
LCB070Z050	70	7	51.4	5	1
LCB080Z050	80	8	45.0	5	1
LCB090Z050	90	9	40.0	5	1
LCB100Z050	100	10	36.0	10	1
LCB200Z050	200	20	18.0	10	1
LCB250Z050	250	25	14.4	10	1
LCB300Z050	300	30	12.0	10	1
LCB400Z050	400	40	9.0	15	1
LCB500Z050	500	50	7.2	15	1
LCB600Z050	600	60	6.0	15	1
LCB700Z050	700	70	5.1	15	1
LCB800Z050	800	82	4.39	15	1
LCB900Z050	900	93	3.87	15	1
LCB1KZ050	1000	105	3.42	15	1



Where high bandwidths and low rise times are required in a small package, the “LC DIP 20” excels. It offers excellent pulse fidelity and low attenuation. Its compatibility with TTL & DTL along with its low profile makes for an extremely popular and adaptable unit for semiconductor and other related applications.

Other delays and impedances are available on special order.

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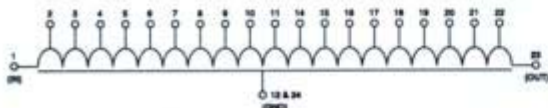
Dual-In-Line 24-Pin Tapped Lumped Constant Delay Lines

Part No.	Time Delay (Nanoseconds)	Rise Time (Nanoseconds)	3dB Bandwidth Minimum (MHz)	Attenuation Maximum (%)	Wiring Diagram No.
IMPEDANCE 75 OHMS					
LCB010Z075	10	2.8	128.5	5	2
LCB020Z075	20	3.0	120.0	5	2
LCB030Z075	30	3.5	102.8	5	1
LCB040Z075	40	4	90.0	5	1
LCB050Z075	50	5	72.0	5	1
LCB060Z075	60	6	60.0	8	1
LCB070Z075	70	7	51.4	8	1
LCB080Z075	80	8	45.0	8	1
LCB090Z075	90	9	40.0	10	1
LCB100Z075	100	10	36.0	10	1
LCB150Z075	150	15	24.0	10	1
LCB200Z075	200	20	18.0	10	1
LCB250Z075	250	25	14.4	10	1
LCB300Z075	300	30	12.0	10	1
LCB400Z075	400	40	9.0	10	1
LCB500Z075	500	50	7.2	15	1
LCB600Z075	600	60	6.0	15	1
LCB700Z075	700	70	5.1	15	1
LCB800Z075	800	82	4.39	15	1
LCB900Z075	900	93	3.87	15	1
LCB01KZ075	1000	105	3.42	15	1
IMPEDANCE 100 OHMS					
LCB010Z100	10	2.0	180.0	5	2
LCB020Z100	20	2.5	144.0	5	1
LCB030Z100	30	3.0	120.0	5	1
LCB040Z100	40	4.0	90.0	5	1
LCB050Z100	50	5.0	72.0	5	1
LCB060Z100	60	6.0	60.0	10	1
LCB070Z100	70	7.0	51.4	10	1
LCB080Z100	80	8.0	45.0	10	1
LCB090Z100	90	9.0	40.0	10	1
LCB100Z100	100	10.0	36.0	10	1
LCB150Z100	150	15.0	24.0	5	1
LCB200Z100	200	20.0	18.0	5	1
LCB250Z100	250	25.0	14.4	10	1
LCB500Z100	500	50.0	7.2	10	1
LCB750Z100	750	75.0	4.8	10	1
LCB01KZ100	1000	100.0	3.6	10	1

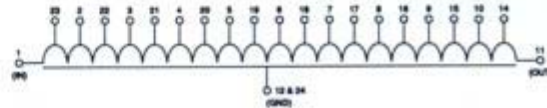
- Time delay to rise time ratio: 10-to-1.
- Temperature Coefficient: less than 100PPM/°C from 0°C to 80°C.
- Total delay tolerance 5% or 1 nanosecond, whichever is greater.
- 20 Taps of equal increments.
- TTL & DTL compatible.
- Low attenuation.
- Exceeds applicable portions of MIL-D-23859
- Voltage rating 50 WVDC



WIRING DIAGRAM No.1



WIRING DIAGRAM No.2



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