

FEATURES

- 8-PIN DIP AND SURFACE MOUNT PACKAGE.
- 5 EQUALLY-SPACED TAPS.
- WIDE OPERATING TEMPERATURE RANGE.
- TOTAL DELAYS FROM 5-100nS.

ELECTRICAL CHARACTERISTICS

- TOTAL DELAY :5NS TO 100NS
- TAP DELAYS :20% INCREMENTS STANDARD
- RISE TIME :20% MAX OF TOTAL DELAY
- PULSE ATTENUATION :SEE TABLE
- CHARACTERISTIC IMPEDANCE :50 TO 200 Ω ($\pm 10\%$)
- DISTORTION : $\pm 10\%$ MAX
- TEMPERATURE COEFFICIENT OF DELAY 100PPM/ $^{\circ}$ C MAX
- OPERATING TEMPERATURE RANGE : 0° C TO $+70^{\circ}$ C
- STORAGE TEMPERATURE RANGE : -65° C TO $+130^{\circ}$ C
- DIELECTRIC STRENGTH :50VDC

ELECTRICAL SPECIFICATIONS

PART NO.	TOTAL DELAY	TAP DELAY (ns)	RISE TIME MAX (ns)	ATTENUATION (%)	IMPEDANCE (Ω)
40P/41P-00551XX	5 \pm 0.5	1 \pm 0.3	3	3	50
40P/41P-01051XX	10 \pm 1	2 \pm 0.4	4	3	50
40P/41P-02551XX	25 \pm 2	5 \pm 0.8	7	3	50
40P/41P-05051XX	50 \pm 2.5	10 \pm 1	15	3	50
40P/41P-07551XX	75 \pm 3.7	15 \pm 1	20	3	50
40P/41P-10051XX	100 \pm 5	20 \pm 1	28	3	50
40P/41P-00512XX	5 \pm 0.5	1 \pm 0.3	3	5	100
40P/41P-01012XX	10 \pm 1	2 \pm 0.4	4	5	100
40P/41P-02512XX	25 \pm 2	5 \pm 0.8	7	5	100
40P/41P-05012XX	50 \pm 2.5	10 \pm 1	15	5	100
40P/41P-07512XX	75 \pm 3.5	15 \pm 1	20	5	100
40P/41P-10012XX	100 \pm 5	20 \pm 1	28	5	100
40P/41P-00522XX	5 \pm 0.5	1 \pm 0.3	3	6	200
40P/41P-01022XX	10 \pm 1	2 \pm 0.4	4	6	200
40P/41P-02522XX	25 \pm 2	5 \pm 0.8	7	6	200
40P/41P-05022XX	50 \pm 2.5	10 \pm 1	15	6	200
40P/41P-07522XX	75 \pm 3.7	15 \pm 1	20	6	200
40P/41P-10022XX	100 \pm 5	20 \pm 1	28	6	200

TABLE I PIN-NUMBER SEQUENCE

CODE	IN	20%	40%	60%	80%	OUT	GROUND
STANDARD	2	3	4	5	6	7	1,8
A	1	2	3	4	6	7	4,8
B	1	7	3	6	4	5	8
C	7	2	6	3	5	4	1,8

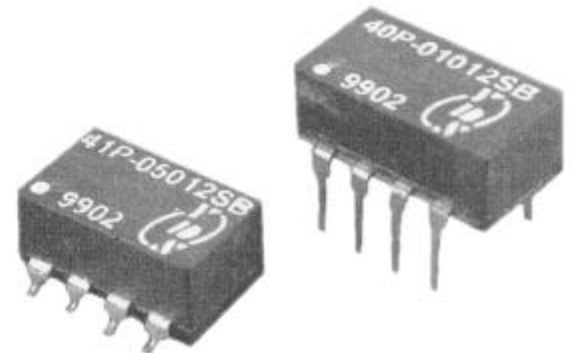
TABLE III
MOUNTING HEIGHT

CODE	DIMENSION(MAX)
A	.200(5.08)
B	.250(6.35)
C	.300(7.62)

Dimensions:inches/mm

Unless otherwise specified, all tolerances are $\pm .010/\pm 0.25$

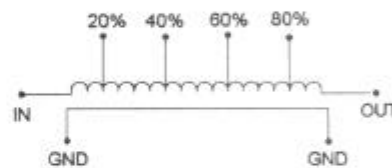
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INPUT PULSE TEST CONDITION

- PULSE VOLTAGE :2.5V
- PULSE WIDTH :5TD
- PULSE RISE TIME :2NS(10% TO 90%)

PIN CONNECTIONS



MARKINGS AND DIMENSION

