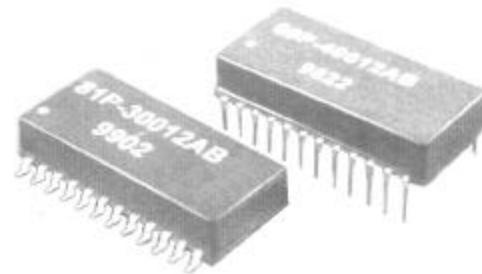


FEATURES

- 24-PIN DIP AND SURFACE MOUNTED PACKAGE.
- 10:1 DELAY-TO-RISE TIME RATION.
- 20 EQUALLY-SPACE TAPS.
- DELAY TIMES FROM 20-5000

ELECTRICAL CHARACTERISTICS

TOTAL DELAY	:20 TO 3000NS
TAP DELAYS	:5% INCREMENTS STANDARD
RISETIME	:10%MAX OF TOTAL DELAY
CHARACTERISTIC IMPEDANCE	:50 TO 200 Ω (±10%)
ATTENUATION	:2 db MAX
DISTORTION	:±10% MAX
TEMPERATURE COEFFICIENT OF DELAY	:100PPM/°C MAX
OPERATING TEMPERATURE RANGE	:0°C TO +70°C
STORAGE TEMPERATURE RANGE	:-65°C TO +130°C
INSULATION RESISTANCE	:1000MΩ MIN @50VDC
DIELECTRIC STRENGTH	:50VDC



ELECTRICAL SPECIFICATIONS

PART NO.	Total Delay nS(±5%)*	Tap Delay nS(±5%)**	Rise (nS)max	Impedance(Ω) (±10%)
80P/81P-02051XX	20	1	3	50
80P/81P-04051XX	40	2	4	50
80P/81P-08051XX	60	3	6	50
80P/81P-08051XX	80	4	8	50
80P/81P-10051XX	100	5	10	50
80P/81P-20051XX	200	10	12	50
80P/81P-02012XX	20	1	3	100
80P/81P-04012XX	40	2	4	100
80P/81P-08012XX	60	3	6	100
80P/81P-08012XX	80	4	8	100
80P/81P-10012XX	100	5	10	100
80P/81P-20012XX	200	10	20	100
80P/81P-30012XX	300	15	30	100
80P/81P-40012XX	400	20	40	100
80P/81P-50012XX	500	25	50	100
80P/81P-60012XX	600	30	60	100
80P/81P-80012XX	800	40	80	100
80P/81P-100012XX	1000	50	100	100
80P/81P-120012XX	1200	60	120	100
80P/81P-150012XX	1500	75	150	100
80P/81P-04022XX	40	2	4	150
80P/81P-08022XX	60	3	6	150
80P/81P-08022XX	80	4	8	150
80P/81P-10022XX	100	5	10	150
80P/81P-20022XX	200	10	20	150
80P/81P-30022XX	300	15	30	150
80P/81P-40022XX	400	20	40	150
80P/81P-50022XX	500	25	50	150
80P/81P-60022XX	600	30	60	150
80P/81P-80022XX	800	40	80	150
80P/81P-100022XX	1000	50	100	150
80P/81P-120022XX	1200	60	120	150
80P/81P-150022XX	1500	75	150	150
80P/81P-200022XX	2000	100	200	150
80P/81P-300033DD	3000	150	300	300

* or ±2ns, whichever is greater.

** * or ±2ns, whichever is greater, referenced from input.

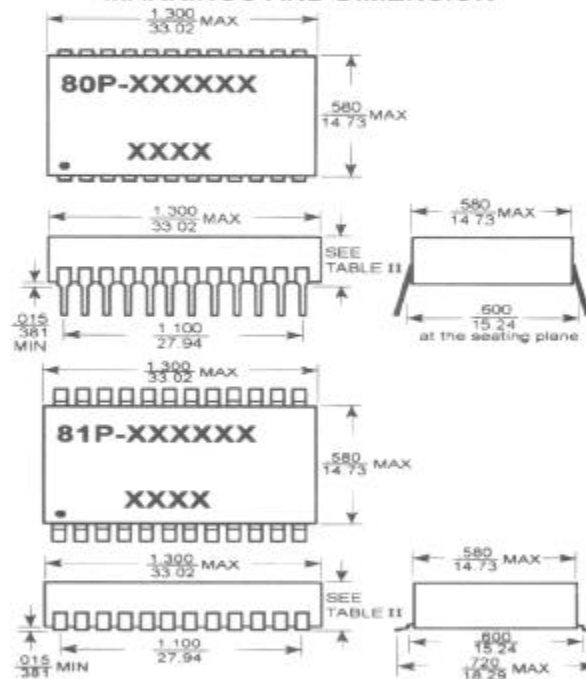
TABLE I. PIN-NUMBER SEQUENCE

CODE	IN	5%	10%	15%	20%	25%	30%	35%	40%	45%	50%	55%	60%	65%	70%	75%	80%	85%	90%	95%	OUT	GND	NC
A	2	3	4	5	6	7	8	9	10	11	12	14	15	16	17	18	19	20	21	22	23	1,24	13
B	2	3	4	5	6	7	8	9	10	11	13	14	15	16	17	18	19	20	21	22	23	12	1,24
C	1	2	3	4	5	6	7	8	9	10	11	14	15	16	17	18	19	20	21	22	23	12,24	13
D	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	1,24	23

INPUT PULSE TEST CONDITION

Pulse Voltage	:2.5V
Pulse Width	:5Td
Pulse Rise Time	:2nS(10% to 90%)

MARKINGS AND DIMENSION



PIN CONNECTIONS

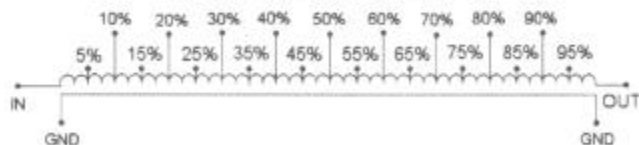


TABLE II. MOUNTING HEIGHT

CODE	DIMENSION(MAX)
B	250(6.35)
C	300(7.62)
D	400(10.16)